

Attorney's Docket No. 16820.P279

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In Re Patent Application of:	)	
	)	
Dinesh Annayya et al.	)	Examiner: Not yet assigned
	)	
Application No.: Not yet assigned	)	Art Unit: Not yet assigned
	)	
Filed: Herewith	)	
	)	
For: CYCLICAL REDUNDANCY	)	
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Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the non-patent literature cited on that form. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicants.

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Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

  X   37 C.F.R. §1.97(b).

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       A statement pursuant to 37 C.F.R. §1.97(e) or

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- (1) A statement pursuant to 37 C.F.R. §1.97(e); and
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 3/16, 2004

  
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Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)	Attorney Docket No.: 16820.P279	Application Number: Not yet assigned
	First Named Inventor: Dinesh Annayya	
	Filing Date: Herewith	

### U.S. PATENT DOCUMENTS

Exam. Initial*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Code <sup>2</sup>	Kind (If known)			
		6,519,737		Derby	2/11/03	
		6,519,738		Derby	2/11/03	

### FOREIGN PATENT DOCUMENTS

Exam. Initial*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup> Code <sup>5</sup>	Number <sup>4</sup>	Kind (If known)				

Examiner Signature		Date Considered	
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<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard S.3). <sup>4</sup>For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

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<b>Substitute for Form 1449A/PTO (Modified)</b> (use as many sheets as necessary)	<b>Attorney Docket No.:</b> 16820.P279	<b>Application Number:</b> Not yet assigned
	<b>First Named Inventor:</b> Dinesh Annayya	
	<b>Filing Date:</b> Herewith	

**OTHER ART - NO PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation <sup>2</sup>
		Louis E. Frenzel, "Programmable Frammer Chip Improves OC-48 Efficiency, Using virtual concatenation, a next-generation Sonet chip allocates bandwidth dynamically, facilitates provisioning, and preclassifies and tags packets to free up network processors.", Electronic Design April 16, 2001, 4 pages.	
		Nilam Ruparelia, "Delivering Ethernet over Sonet using Virtual Concatenation", CommsDesign, February 25, 2002, 8 pages.	
		Michael Timothy Moore, "Filling the WAN-communications-line card", <a href="http://www.ednmag.com">www.ednmag.com</a> , April 18, 2002, p. 83-90.	
		"A Brief Explanation of CRC Computation", <a href="http://www.ece.msstate.edu/~reese/EE4743/crcexplained.htm">http://www.ece.msstate.edu/~reese/EE4743/crcexplained.htm</a> , downloaded 1/16/04, 2 pages.	
		Eric E. Johnson, "High-Speed Computation of Cyclic Redundancy Checks", NMSU-ECE-95-011, November 1995, 14 pages.	

<b>Examiner Signature</b>		<b>Date Considered</b>	
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